AN INVESTIGATION INTO THE APPLICABILITY OF NATURAL LOAD VARIATION SCHEME TO THE MEASUREMENT OF ENERGY YIELD OF PHOTOVOLTCIT MODULE

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ABSTRACT

Currently, photovoltaic (PV) modules are characterized based on open circuit voltage, short circuit current as well as voltage and current at maximum power point under strictly specified laboratory conditions. Among manufacturers, regulators and experts, this approach appears reasonably adequate. Nevertheless, among end users and and low level technicians, it may be misleading. This is on account of the critical difference between the laboratory and in situ conditions. This often results to improper design which in turn is capable of accelerating a premature system failure. The present work explores the potentials of natural load variation scheme as a low cost option that is capable of estimating the actual yield of PV modules. Essentially, the scheme consists of a firmware controlling the switching of a number of resistors (loads) connected in parallel. By looping through the resistors in parallel, the firmware matches load impedance to the impedance of the module thus the module operates at its maximum power point. The research results indicate a similar response pattern for constant and variable loads. Nevertheless, the quantitative value of recorded voltage, current, power and energy tended to increase as the number of available resistors increased. Though clear convergence was not achieved, natural load variation scheme more realistically captures the yield potentials of polycrystalline PV modules under low irradiance conditions.

Keywords – impedance matching, energy yield, natural load variation, maximum power point.

1. INTRODUCTION

Solar modules produce direct current (DC) electricity. They are usually rated/classified by the power they deliver under specified conditions. Common conditions include: standard test condition (STC) and Photovoltaics for Utility Scale Application (PVUSA) Test Conditions (PTC). Under Standard Test Condition (STC) protocol, solar modules are characterized in a controlled environment where the cell temperature is maintained at 25°C. With the aid of solar simulator, the irradiance is kept at 1000 W/m² while the absolute air mass is kept at 1.5. Under Photovoltaics for Utility Scale Application (PVUSA) Test Conditions (PTC) characterization procedure, the module is kept ten meters above ground while its ambient temperature is maintained at 20°C. Wind speed of 1 m/s and absolute air mass of 1.5 are also kept.

Arguably, these conditions can be recreated in standard laboratories. Unfortunately, solar modules operate under non controlled conditions where various environmental and design factors actually affect the primary purpose of their installation-energy yield [1]. This means that the present rating parameters do not give enough information about the quantity of energy a given PV module will deliver in actual operation when subjected to varying and not easily predictable environmental variables [2].

2. REVIEW OF RELATED WORKS

Quite a considerable volume of literature has been devoted to the investigation of the impact of varying environmental conditions as well as in situ design and installation practices on the energy yield of solar modules [3-5]. These efforts have resulted in a number of models which seek to predict the performance of solar module outside standard test conditions. These models often seek to estimate how departure from standard test conditions affect the performance of the module and so make necessary corrections that would give a fair estimate of the modules’ outdoor performance.

The single diode model of the solar cell, shown in Figure 1 is one of the most widely adopted models in use [6]. It is based on the equivalent circuit of the solar cell. From the single diode model above, is derived the characteristic equation of solar cell [6]:

$$I_t = I_R - I_o \left( e^{\frac{q(V_t+I_t R_s)}{kT}} - 1 \right) - \frac{V_t + I_t R_s}{R_{sh}}$$  (1)

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From equation (1), it is obvious that junction temperature of the solar cell is a key performance parameter of the solar cell. Since it has inverse relationship with I_L, it imparts negatively on the output current from solar cells. Manufacturers often supply the temperature coefficients of their modules where α, β and γ represent temperature coefficients of current, voltage and power respectively. Various research findings also indicate that the temperature coefficients also tend to vary with irradiance level [7] [8]. It is important, therefore, to take note of cell temperature while predicting yield of a PV module.

\[ T_{cell} = T_{air} + (NOCT - 20) \times \frac{E}{80} \]  

(2)

In (2) E is the irradiance at the given time. In applying equation (2), as well as other possible correlations [10] it is important to recognize the limitations imposed by the site specific nature of most of the correlations as already demonstrated by various researchers [11, 12].

2.1 From Standard Test Condition (STC) To Natural Environmental Condition (NEC)

At present the standard characterizing parameters supplied by PV module manufacturers include: the open circuit voltage (V_{oc}), the short circuit current (I_{sc}), the voltage, current and power at maximum power point (V_{mpp}, I_{mpp} and P_{mpp} respectively) under the STC. These parameters, though determined by other manufacturing variables are differently affected by temperature and other environmental variables. Researchers [13] have shown that the following relationship shown in equation (3) exists between α and I_{sc}

\[ I_{sc}(\text{at any other temp}) = I_{sc}(\text{at STC})(1 + \alpha \Delta T) \]  

(3)

Similarly, the relationship shown in equation (4) exists between β and V_{oc}

\[ V_{oc}(\text{at any other temp}) = V_{oc}(\text{at STC})(1 - \beta \Delta T) \]  

(4)

For monocrystalline and polycrystalline both α and β have negative coefficients hence while output current increases with temperature, Voc tends to decrease with temperature as shown in Figure 2. A close look at Figure 2 shows that 0.05 % increase in I_{sc} occasioned by temperature is disproportionately counteracted by the 0.5% decrease in the Voc. Situations such as this offsets the projected power delivery of real life PV system. There is therefore, a need to translate from STC to Natural Environment Conditions (NEC).

Such translations involve careful study of how environmental variables affect the behavior of an ideal solar cell [15] [6]. They often lead to the development of equations. In the adaptation of these equations it is necessary to understand the specificity of some of the equations [16]. In such situations, site specific relations may be necessary.

2.2 Energy Yield as a Characterizing Parameter

Though at present, solar PV modules are generally rated in terms of Voc, Isc, Vmp, Imp and Pmax researchers are seeking other [17]. One of such methods is the use of energy yield as elucidated by the National Renewable Energy Laboratory (NREL) USA. NREL has shown that it is possible to find a consensus among various energy yield procedures under outdoor conditions and have developed energy yield calculators based on equation (5) [18] [19]:

\[ E = A \times r \times H \times PR \]  

(5)

In (5), E is the Energy (kWh), A is the Total solar panel Area (m²), r is the solar panel yield given by the ratio of
maximum power in kW to the area in m², H is the annual average solar radiation on tilted panels (shadings not included) and PR is the performance ratio, coefficient for losses (range between 0.5 and 0.9, default value = 0.75) The problem with this procedure is that it depends on a yearly average which may not reflect ever changing weather variables. Researchers have also attempted a physical model for estimation of daily Energy yield by using equation (6) [20].

\[
E = \sum_{t=0}^{24} P_{stc} \times \left\{ G(t) \times \frac{t}{1000} \right\} \\
\times \left\{ 1 + \frac{\gamma_p \text{mp} (T(t) - 25)}{100} \right\} \times f_{dirt} \times f_{mm} \times f_{cable} \times f_{inv}
\]

(6)

Where: \( f_{dirt} \) = correction factor in respect of dirt, 
\( f_{mm} \) = correction factor in respect of mismatch 
\( f_{cable} \) = correction factor in respect of cable losses 
\( f_{inv} \) = correction factor in respect of inverter 
\( \gamma_p \text{mp} \) = Temperature coefficient for P_{mp} (%/°C)

Here there still exists the issue of STC rating, though standard translation procedures were introduced. Again average irradiance is replaced with sampled irradiance which represents some improvement but sampling time is yet high since instantaneous irradiance tend to change in seconds. Researchers [21] have also reviewed the use of Artificial Neural Method in the estimation of energy yield of solar PV and reported a successful fusion of physical and artificial neural methods in their work. The International Electrotechnical Commission (IEC) developed energy rating standard IEC 61853 [22]. It provides guidelines on energy rating by calculating the energy yield on the basis of PV module characteristics and parameters, including irradiance, temperature, incident angle and spectral distribution. The calculations are performed using either given standard days or site-specific measured data. In the evaluation of site-specific data, the effort required of measurement equipment is enormous. In particular, the direct beam radiation Gdir and the solar spectrum E(λ) have to be measured among many other parameters [23].

2.3 The Load Variation Approach

A clear conclusion arising from the review of several works already cited is that the yield of solar modules is affected by so many factors some of which are site specific. Earlier works have sought to characterize and factor in these variables in an effort to make fairly accurate global projection on the yield of solar modules. While these efforts may be quite useful to experts imbued with sophisticated measuring instrument, they do not sufficiently equip the low end technicians and owners of PV modules with a fairly simple method of determining the energy yield potentials of their module. Existing characterization of PV module often capture performance at maximum power point. The standard characterization procedure often involve expensive laboratories and complex measuring instruments. Accordingly, researchers in the less developed world often resort to using single resistive load to study the dynamic response of PV module to irradiance and other environmental variables. This can lead to a misreading of the power and yield of a given PV module. This erroneous methodology has led researchers to arrive at misleading conclusions such as [24] which indicated the non viability of PV modules as a serious source of power in south eastern Nigeria. The natural load variation scheme being proposed is , simply put, an impedance matching algorithm that dynamically seeks to match load impedance to the dynamic impedance developed along the IV curve of the PV module in response to changes in irradiance and other significant environmental variables. The basic goal is to get the module to operate around its maximum power point. As evident from the nature of the flowchart, shown in Figure 3, the process is an endless loop. It continues until it is forcefully stopped. At startup, an Arduino based firmware gradually switch an N array of resistors arranged in parallel from R=1 to R=N(thus changing the effective value of the total resistance seen). At each instance, the voltage , current and power are read out and stored by the microcontroller. The firmware contains a sub routine which loops through all the calculated power for different values of current and voltage as different numbers of resistors are switched on. The minimum calculated power calculated in the course of loop becomes Pmin. The maximum value becomes the Pmax. Pmax is subsequently chosen as the reference. Meanwhile the microcontroller keeps reading the voltage and current at the maximum power point. The power gotten at this point is described as the instantaneous power (Pinst). The moment the instantaneous power records a 10% deviation from the chosen maximum power point, the system automatically finds the new N number of resistors that would ensure maximum power transfer from the panel.

3. METHODOLOGY

For the purpose of demonstrating the effect of varying the number of resistors in the natural load variation scheme, an experiment was conducted using Arduino mega (a microcontroller),three 30W PV modules, a number of choke resistors and TIP 31. The set up is shown in Figure 4.
Prior to the experiment, the optimal load resistance of the modules, at rated maximum power point, was determined from the rated current and voltage at maximum power point using Ohm's law - \( R = \frac{V}{I} \). The current at maximum power point is given as 1.71A while the voltage at maximum power point is given as 17.5V, hence load resistance at rated maximum power point is calculated to be 10.23 \( \Omega \). This serves as the low end optimal resistance i.e. the dynamic module resistance when an irradiance of 1000W/m\(^2\) falls on the module when the cell temperature is 25°C. The high end would ideally exist during open circuit when \( R \) is infinite.

To achieve this, a number of parallel resistors needed to be placed in parallel so that as the need arises an \( N \) number of resistors would be switched. To find a general a reference was made to the traditionally formula for calculating the equivalent resistance of resistors in parallel. Assuming a case of four resistors in parallel

\[
\frac{1}{R_{eq}} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3} + \frac{1}{R_4}
\]

This can be rearranged such that

\[
\frac{1}{R_{eq}} = \frac{R_2R_3R_4 + R_1R_3R_4 + R_1R_2R_4 + R_1R_2R_3}{R_1R_2R_3R_4}
\]

If \( R_1 = R_2 = R = 3\Omega = R_4 \) equation 8 could easily transform into

\[
R_{eq} = \frac{R^4}{R^3 + R^3 + R^3 + R^3}
\]
Generalized, equation (9), could easily become

\[ R_{eq} = \frac{R^N}{NR^{N-1}} \]  

(11)

It can also be further simplified into:

\[ R_{eq} = \frac{R}{N} \]  

(12)

Applying the low end and high end and high end optima to equation 12

\[ N = \frac{R_{eq}}{R} \]  

(13)

Plugging into the equation, optimal \( N = \frac{225}{10.3} \approx 22 \).

To determine the power rating of the individual resistors used, it was noted that following simple application of Ohm’s law, maximum current will flow when the entire N resistors in parallel are on. For the purposes of simplification, it was necessary to assume that all the resistances were in fact identical and that current was evenly shared among them. The maximum current roughly corresponds to the maximum rated current the given module would deliver. The minimum power ratings of the individual resistors were determined theoretically using the following relations:

\[ P_{diss} = I_{max}^2 R \]  

(14)

In (14), \( I_{max} = I_{mp}/R \). The calculated results are shown in Table 1. Since the calculated power represents the minimum, higher values were used for the experiment as also shown in Table 1.

<table>
<thead>
<tr>
<th>( N )</th>
<th>( R_{\text{calculated}} )</th>
<th>( R_{\text{available}} )</th>
<th>( P_{\text{calculated}} )</th>
<th>( P_{\text{used}} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>10.3Ω</td>
<td>10 Ω</td>
<td>30.625W</td>
<td>40W</td>
</tr>
<tr>
<td>4</td>
<td>40 Ω</td>
<td>39 Ω</td>
<td>7.31W</td>
<td>10W</td>
</tr>
<tr>
<td>12</td>
<td>120 Ω</td>
<td>120 Ω</td>
<td>2.4W</td>
<td>5W</td>
</tr>
<tr>
<td>22</td>
<td>220 Ω</td>
<td>220 Ω</td>
<td>1.33W</td>
<td>5W</td>
</tr>
<tr>
<td>27</td>
<td>270 Ω</td>
<td>270 Ω</td>
<td>1.08W</td>
<td>5W</td>
</tr>
</tbody>
</table>

To provide the natural load variation, a certain number (N) of resistors were placed in parallel. An Arduino based firmware was developed. The development of the firmware was modularized. At start up, the check_Mpower function is called up to switch different number of resistors by looping from R=1 to R=N. It then chooses an R value that delivers the maximum power for a given irradiance, temperature and other relevant variables. The turn_on_load function switches the appropriate number of resistors. This process is repeated whenever there is a 10% variation in recorded power. To achieve this, check_Instpower function regularly monitors instantaneous power generated as a result of variations in weather condition. A datalogging function was also developed. Two files were created and regularly updated after every two minutes. One file recorded the switching behavior while the other one recorded voltage, current, power and yield. In either case, common statistical methods were used to generate mean and standard deviation.

3.2 Measurements And Error Analysis
A simple voltage divider rule was applied for voltage measurement. To meet the requirement for high input impedance in voltage measurement, 1 MΩ and 10 MΩ resistors were used to form the voltage divider. This enables a voltage of up to 40V to be safely measured on a 5V microcontroller. The rated tolerance of the resistor was 10%. To compensate for this, the multiplication factor was individually determined by calibration against Fluke 117 true rms multimeter. The analogRead() of the Arduino microcontroller can introduce random error of up to 5%. To reduce this error, a function that performed statistical analysis was introduced. The mean of fifty readings was used for energy calculation and the standard deviation of the readings recorded. An extraordinarily high standard deviation was regarded as indicative of serious error hence such readings were discarded.

3.3 Energy Yield Calculation
To calculate the energy yield of a given PV module, the current and voltage measurement procedure described above were used to generate voltage and current values which were multiplied to generate power. On account of the constantly changing values of voltage and current, the energy yield at a given time was calculated by multiplying power by the elapsed time. A process of continuous addition was then used to determine the total energy yield.

4. RESULT AND DISCUSSION
Under a sustained low irradiance level, systems with N>1 progressively tended to generate comparatively higher power as shown in figure 7. Under a sustained high irradiance level, the systems tended to perform equally except for slight variations attributable to differences in the value of total resistance as seen by the system. Resistances slightly lower than the calculated resistance at the rated maximum power point tended to generate higher power. Under situations of high swing in irradiance values, the system with N=4 tended to respond faster and attain stability and was better at making an optimal choice of N. The system with N=15 took longer time to settle under such situations. The result is often wrong choice of optimal N which empirical evidence show negatively affected the power generated. This is because accurate choice of N was necessary for current calculation of current.
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Figure 5: Graph of voltage against time

Figure 6: Graph of current against time
Figure 7: Graph of power against time

Figure 8: Graph of energy against time
This finding is in agreement with recent research findings [25 – 28] on dependence of shunt and series resistance on irradiance at low irradiance levels. The authors demonstrate that at low irradiance shunt resistance tended to increase with decrease in irradiance while series resistance tended to decrease with decrease in irradiance. The curve is shown to be an inverse of I-V curve. Hence series resistance can be roughly determined by calculating the inverse of the slope of the I-V curve at the open circuit voltage and the shunt resistance from the inverse of the slope of the I-V curve at the short circuit condition V=0. Maroor Ahmed [29] in his analysis based on the five parameter single diode showed that while the fill factor varied inversely with series resistance, its relationship with shunt resistance is an inverse of the characteristic I-V curve.

5. CONCLUSIONS
The load variation approach provided better way of studying the outdoor performance of a PV module. This is because it provides a dynamic impedance matching between load and PV in the course of the dynamic interplay of the various environmental and design factors that are known to affect the yield of the PV module. As the number of N increased, there was the graph power against time tended to converge. Final convergence was not recorded. Hence the system may not, on its own, provide an absolutely correct measure of energy yield. A study is ongoing to discover a correcting equation that would enable an absolutely correct measurement when R number of resistors is used. Meanwhile, natural load approach would enable outdoor measurement of PV module under any environmental condition. The observed limitation on its usefulness in absolute measurement of energy yield may be compensated via a comparative approach. Under this approach, the yield of a properly characterized module may be compared to that of another module whose characterization status is questionable. In this way the advantage of load variation approach would be used while its shortfall would be avoided.

6. ACKNOWLEDGEMENT
The authors wish to acknowledge the contribution of Chinedu Mama and Chrisantus Obinna Nnamani in the course of experiment and documentation of this work.

7. REFERENCES


